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APPLICATION NO. FILING DATE		ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/778,837		02/08/2001	Norihiro Nakatsuhama	1614.1123	6812		
21171	7590	09/02/2005		EXAMINER			
STAAS & HALSEY LLP SUITE 700				BHAT, ADITYA S			
	YORK A	VENUE, N.W.	ART UNIT	PAPER NUMBER			
WASHING			2863				

DATE MAILED: 09/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	on No.	Applicant(s)	<del></del>				
		09/778,83	· .7	NAKATSUHAMA ET AL.					
Office	e Action Summary	Examiner		Art Unit		-MW-			
÷		Aditya S. E	3hat	2863		V.			
	LING DATE of this communicatio	1		orrespondence ac	dress				
Period for Reply									
WHICHEVER IS  - Extensions of time after SIX (6) MONT  - If NO period for rep  - Failure to reply with Any reply received	O STATUTORY PERIOD FOR R S LONGER, FROM THE MAILIN may be available under the provisions of 37 C HS from the mailing date of this communicatily is specified above, the maximum statutory in the set or extended period for reply will, by the Office later than three months after the adjustment. See 37 CFR 1.704(b).	NG DATE OF TH CFR 1.136(a). In no eve ion. period will apply and will statute, cause the appl	IIS COMMUNICATION ont, however, may a reply be timed to the spire SIX (6) MONTHS from the ication to become ABANDONE	N. nely filed the mailing date of this c D (35 U.S.C. § 133).	·				
Status									
1)⊠ Responsi	ve to communication(s) filed on	22 June 2005.							
2a) This action	n is <b>FINAL</b> . 2b)⊠	This action is no	on-final.						
3) Since this	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is								
closed in	accordance with the practice un	ider <i>Ex parte Qu</i>	<i>ayle</i> , 1935 C.D. 11, 45	53 O.G. 213.					
Disposition of Cla	ims								
4) Claim(s)	1-8 is/are pending in the applica	ition.							
•	4a) Of the above claim(s) is/are withdrawn from consideration.								
5) Claim(s)	☐ Claim(s) 8 is/are allowed.								
6)⊠ Claim(s)	☑ Claim(s) <u>1-5 and 7</u> is/are rejected.								
·	<u>6</u> is/are objected to.								
8)∐ Claim(s) <sub>.</sub>	are subject to restriction a	and/or election re	equirement.						
Application Paper	S								
9)∐ The speci	fication is objected to by the Exa	aminer.							
10)⊠ The drawi	ng(s) filed on 28 February 2001	is/are: a)⊠ acc	epted or b) Objecte	d to by the Exami	ner.				
Applicant r	may not request that any objection t	to the drawing(s) b	e held in abeyance. See	∋ 37 CFR 1.85(a).					
•	ent drawing sheet(s) including the c	•	=	=		(d).			
11) The oath	or declaration is objected to by t	he Examiner. No	te the attached Office	Action or form P	10-152.				
Priority under 35 l	J.S.C. § 119								
12) Acknowled	dgment is made of a claim for fo	reign priority und	der 35 U.S.C. § 119(a)	)-(d) or (f).					
•	☐ Some * c)☐ None of:								
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• •	ached detailed Office action for	· -		ad.	•				
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Attachment(s)  1) Notice of Referen	ces Cited (PTO-892)		4) Interview Summary	(PTO-413)					
2) District of Draftspe	erson's Patent Drawing Review (PTO-94		Paper No(s)/Mail Da	ate					
3) Information Disclo Paper No(s)/Mail	osure Statement(s) (PTO-1449 or PTO/S Date	SB/08)	5) Notice of Informal P 6) Other:	atent Application (PT	O-152)				

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#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5 and 7 rejected under 35 U.S.C. 102(b) as being anticipated by Takaba et al. (USPN 5,565,856).

Takaba et al. (USPN 5,565,856) teaches the limitations of Claim 1 as follows; an abnormality detection device for detecting an abnormality in a communication bus, the device comprising of

a timer counter to measure a time during which a logical output of said communication bus remains at a first logical level which is a high level or a low level (825;See Figure 9, 945;Figure 24 & (Col.7& 8, lines 64-67 & 1-24) and

a comparator to compare the time measured by said timer counter with a threshold value and to output an abnormality detection signal indicating an abnormality in said communication bus when the time surpasses said threshold value, (Col. 6, lines 4-16) wherein the abnormality detection device is independent of a CPU controlling the communication bus (300; figure 1a)

Claim 5 has all the limitations of claim one with an additional timer (Col. 1, lines 50-56) and a register for adding the time measured by one of the timers (Col.7, line 40-45)

With regards to Claim 7, Takaba et al. (USPN 5,565,856) teaches a microcomputer (120;See Figure 1a) connected to a communication bus, the microcomputer comprising

a timer to measure a time during which a logical output of said communications .
bus remains at a first logical level which is a high or low level; (Col. 6, line 3-16) and

a comparator to compare the time measured by said timer counter with a threshold value and to output an abnormality detection signal indicating an abnormality in said communication bus when the time surpasses said threshold value, (Col. 6, lines 4-16) wherein the abnormality detection device is independent of a CPU controlling the communication bus (300; figure 1a)

Regarding claims 2-3, Takaba et al. (USPN 5,565,856) teaches initializing the timer and comparator. (See figure 9)

Regarding Claims 4, Takaba et al. (USPN 5,565,856) teaches plurality of registers and a means for selecting values stored in those registers. (1020;Figure 11)

### Allowable Subject Matter

Claim 6 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 6 and 8:

The primary reason for the allowance of claims 6 & 8 is the inclusion of: wherein said register supplies said cumulative time to at least one of said at least two timer counters, and said at least one of said at least two timer counters measures the time by using said cumulative time as an initial value. It is this feature found in the claim, as it is claimed in the combination that has not been found, taught or suggested by the prior art of record, which makes this claim allowable over the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### Response to Arguments

Applicant is reminded that during patent examination, the pending claims must be "given the broadest reasonable interpretation consistent with the specification." Applicant always has the opportunity to amend the claims during prosecution, and broad interpretation by the examiner reduces the possibility that the claim, once issued, will be interpreted more broadly than is justified. In re Prater, 415 F.2d 1393, 1404-05, 162 USPQ 541, 550-51 (CCPA 1969).

While the meaning of claims of issued patents are interpreted in light of the specification, prosecution history, prior art and other claims, this is not the mode of claim interpretation to be applied during examination. During examination, the claims must be interpreted as broadly as their terms reasonably allowed. This means that the words of the claim must be given their plain meaning unless applicant has provided a

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clear definition in the specification. In re Zletz, 893 F.2d 319, 321, 13 USPQ2d 1320, 1322 (Fed. Cir. 1989).

In this instance applicant argues that the prior art of record does not teach the claimed timer counter which measures " a time during which logical output of said communication bus remains at a first logic level which is a high level or low level (Col 6, lines 3-16), and that the abnormality detection device is independent of a CPU controlling the communication bus (110,120,130,200; figure 1a) (Col. 5 lines 37-39)

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Aditya S Bhat whose telephone number is 571-272-2270. The examiner can normally be reached on M-F 9-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on 571-272-2269. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Aditya Bhat August 31, 2005

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